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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Anderson *et al.*

Conf. No.: 1115

Serial No.: 10/604,116

Art Unit: 2823

Filed: 06/26/2003

Dkt. #: BUR920030031US1 (IBMB-0028)

Title: METHOD OF FORMING
FREESTANDING
SEMICONDUCTOR LAYER

Examiner: Garcia, Joannie A

COMMISSIONER FOR PATENTS

DESTINATION FACSIMILE NUMBER: 703-872-9306

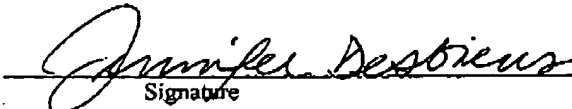
Transmitted herewith is: **Response to Office Communication in 2 pages**

in the above identified application.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date shown below.

Jennifer Desbiens
(Person transmitting this correspondence)


Signature Date April 1, 2005

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE COMMUNICATION

Sir:

This paper is being filed in response to the Office Communication dated March 21, 2005.

With respect to the election of a specific group of claims from the Restriction Requirement, Applicants hereby provisionally elect Group I, claims 1-10 and 16-20, with traverse. Applicant's arguments are presented in the previously filed Response to Restriction Requirement.

Applicant appreciates the office providing clarification regarding the groupings of claims, as requested in the previous Response. Prompt examination on the merits is respectfully requested.

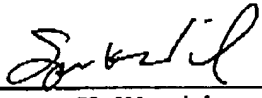
10/604,116

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Should the examiner require anything further from Applicants, the examiner is invited to contact Applicants' undersigned representative at the number listed below.

Respectfully submitted,

Date: April 1, 2005



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